

Searching PAJ

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 02-058365

(43)Date of publication of application : 27.02.1990

(51)Int.Cl.

H01L 27/04
H03B 5/32

(21)Application number : 63-208257

(71)Applicant : NEW JAPAN RADIO CO LTD

(22)Date of filing : 24.08.1988

(72)Inventor : ISHIDA RIICHI

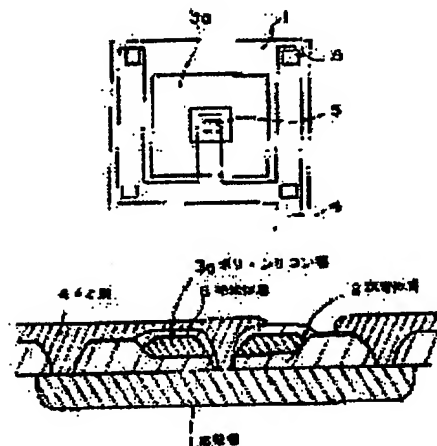
(54) INTEGRATED CIRCUIT

(57)Abstract:

PURPOSE: To reduce the internal resistance of a MOS type capacitor so as to make it easy to oscillate and to improve frequency stability by forming the other electrode to the diffusion layer region of one electrode of a MOS type capacitor in doughnut shape, and bringing the metallic film for taking out electrode into contact with the center, too, of the diffusion region.

CONSTITUTION: An electrode consisting of a polysilicon layer 3a is made in doughnut shape, and an Al film 4 is brought into contact with the center, too, of a diffusion layer 1 through the opening at the center provided in the polysilicon layer 3a. Hereby, the resistance amounts entering serially into the capacity decreases.

Accordingly, as compared with the equivalent resistance of a crystal oscillator, it can be restrained enough small, and it oscillates without fail, and the frequency stability becomes good.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

<http://www19.ipdl.ncipi.go.jp/PA1/result/detail/main/wAAA6haO7tDA402058365...> 2006/01/26

Searching PAJ

Copyright (C); 1998,2003 Japan Patent Office